Research Experiences for Teachers (RET)
CHIP DESIGN RESEARCH EXPERIENCE FOR TEACHERS
2024-2025

The NSF Research Experience for Teachers (RET) is a year-long program for Stillwater and neighboring area high school and community college teachers (Grades 9-14). This RET includes a 6-week summer internship on semiconductors and chip design at Oklahoma State University. Participants must also translate their research experience into new curriculum modules.

Program Dates
June 3- June 12, 2024
Hours: 9:00am - 4:00pm
Hybrid Format: In-person + Zoom

Summer Location
Oklahoma State University
226 Engineering South,
Stillwater, OK 74078

Internship Benefits:
- $8,000 stipend
- Up to $2,000 of additional awards upon meeting program criteria
- 6-week internship in an integrated circuit (IC) design laboratory to learn about semiconductors and chip design fundamentals
- Support in developing inquiry-based pedagogy and lesson plans
- Network with fellow STEM teachers
- Opportunity to earn a free trip to Washington, DC, to present your work

How to Apply
Visit our Website at
ceat.okstate.edu/stem/ret
To learn more about the Chip RET internship and apply!

Apply Here

Eligibility
- Current STEM teachers in high school or community college
- No prior knowledge of chip design is required

Internship Structure
- Research under an OSU faculty and a graduate student mentor
- Individual and group meetings to discuss research experience
- Submit your lesson plan to TeachEngineering.org

COLLEGE OF
ENGINEERING, ARCHITECTURE AND TECHNOLOGY

Questions?
Contact
John Hu, Assistant Professor
Electrical and Computer Engineering
Oklahoma State University
John.hu@okstate.edu

STEM
SCIENCE TECHNOLOGY ENGINEERING MATHEMATICS